

## Product Specification

### 14 Gb/s Short-Wavelength SFP+ Transceiver

#### FTLF8529P3BNV

#### PRODUCT FEATURES

- Up to 14.025 Gb/s bi-directional data links
- Hot-pluggable SFP+ footprint
- Built-in digital diagnostic functions
- Built-in CDR with shut off control
- 850nm Oxide VCSEL laser transmitter
- Duplex LC connector
- RoHS compliant and Lead Free
- 35m on 50/125µm MMF
- 100m on high-bandwidth 50/125um (OM3) MMF
- Metal enclosure, for lower EMI
- Single 3.3V power supply
- Operating temperature range:
  - 0°C to 85°C



#### APPLICATIONS

- Tri-Rate 4.25/8.5/14.025 Gb/s Fibre Channel

Finisar's FTLF8529P3BNV SFP+ transceivers are designed for use in Fibre Channel links up to 14.025 Gb/s data rate over multimode fiber. They are compliant with FC-PI-5 Rev. 6.00a, SFF-8472 Rev 11.0c and SFF-8081h, and compatible with SFF-8432b and applicable portions of SFF-8431 Rev. 4.1d. The optical transceivers are compliant per the RoHS Directive 2011/65/EU. See Finisar Application Note AN-2038 for more details.

#### PRODUCT SELECTION

**FTLF8529P3BNV**

## 4. Pin Descriptions

Pin	Symbol	Name/Description	Ref.
1	V <sub>EET</sub>	Transmitter Ground (Common with Receiver Ground)	1
2	T <sub>FAULT</sub>	Transmitter Fault.	2
3	T <sub>DIS</sub>	Transmitter Disable. Laser output disabled on high or open.	3
4	SDA	2-wire Serial Interface Data Line (MOD-DEF2)	4
5	SCA	2-wire Serial Interface Clock (MOD-DEF1)	4
6	MOD_ABS	Module Absent, connected to V <sub>EET</sub> or V <sub>EER</sub>	4
7	RS0	Rx Rate Select: Open or Low = 8.5 or 4.25 Gb/s Fibre Channel (Low Bandwidth) High = 14.025 Gb/s Fibre Channel (High Bandwidth)	5
8	LOS	Loss of Signal indication. Logic 0 indicates normal operation.	6
9	RS1	Tx Rate Select: Open or Low = 8.5 or 4.25 Gb/s Fibre Channel (Low Bandwidth) High = 14.025 Gb/s Fibre Channel (High Bandwidth)	5
10	V <sub>EER</sub>	Receiver Ground (Common with Transmitter Ground)	1
11	V <sub>EER</sub>	Receiver Ground (Common with Transmitter Ground)	1
12	RD-	Receiver Inverted DATA out. AC Coupled	
13	RD+	Receiver Non-inverted DATA out. AC Coupled	
14	V <sub>EER</sub>	Receiver Ground (Common with Transmitter Ground)	1
15	V <sub>CCR</sub>	Receiver Power Supply	
16	V <sub>CCT</sub>	Transmitter Power Supply	
17	V <sub>EET</sub>	Transmitter Ground (Common with Receiver Ground)	1
18	TD+	Transmitter Non-Inverted DATA in. AC Coupled.	
19	TD-	Transmitter Inverted DATA in. AC Coupled.	
20	V <sub>EET</sub>	Transmitter Ground (Common with Receiver Ground)	1

Notes:

- Circuit ground is internally isolated from chassis ground.
- T<sub>FAULT</sub> is an open collector/drain output, which should be pulled up with a 4.7k – 10k Ohms resistor on the host board if intended for use. Pull up voltage should be between 2.0V to V<sub>cc</sub> + 0.3V. A high output indicates a transmitter fault caused by either the TX bias current or the TX output power exceeding the preset alarm thresholds. A low output indicates normal operation. In the low state, the output is pulled to <0.8V.
- Laser output disabled on T<sub>DIS</sub> >2.0V or open, enabled on T<sub>DIS</sub> <0.8V.
- Should be pulled up with 4.7k – 10kohms on host board to a voltage between 2.0V and 3.6V. MOD\_ABS pulls line low to indicate module is plugged in.
- Rate select can also be set through the 2-wire bus in accordance with SFF-8472 v. 11.0<sup>c</sup>. Rx Rate Select is set at Bit 3, Byte 110, Address A2h. Tx Rate Select is set at Bit 3, Byte 118, Address A2h. Note: writing a “1” selects maximum bandwidth operation. Rate select is the logic OR of the input state of Rate Select Pin and 2-wire bus.
- LOS is open collector output. Should be pulled up with 4.7k – 10kohms on host board to a voltage between 2.0V and 3.6V. Logic 0 indicates normal operation; logic 1 indicates loss of signal.

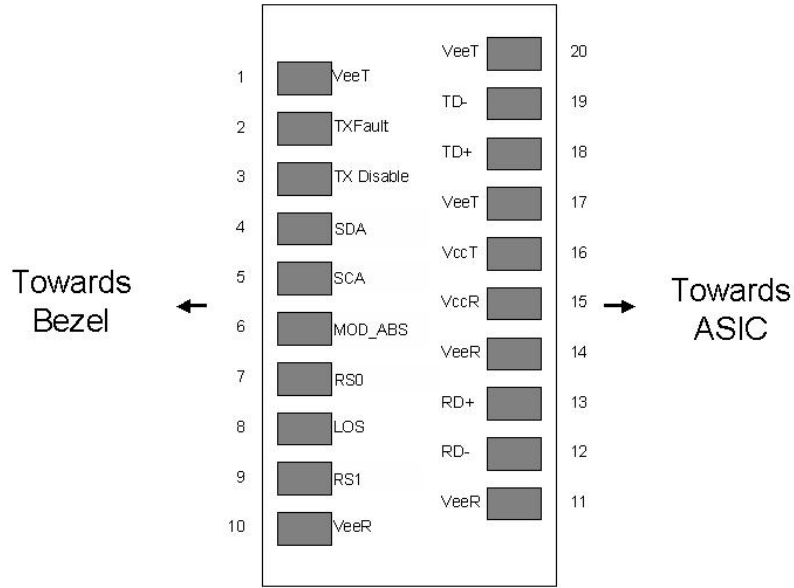


Diagram of Host Board Connector Block Pin Numbers and Names

#### 4. Absolute Maximum Ratings

Parameter	Symbol	Min	Typ	Max	Unit	Ref.
Maximum Supply Voltage	V <sub>cc</sub>	-0.5		4.0	V	
Storage Temperature	T <sub>s</sub>	-40		85	°C	
Case Operating Temperature	T <sub>c</sub>	0		85	°C	
Relative Humidity (Non-condensing)	RH	0		85	%	

**III. Electrical Characteristics ( $T_c$ ,  $V_{CC} = 3.15$  to  $3.46$  Volts)**

Parameter	Symbol	Min	Typ	Max	Unit	Ref.
Supply Voltage	$V_{CC}$	3.15		3.46	V	
Supply Current	$I_{CC}$			285	mA	1
<b>Transmitter</b>						
Input differential impedance	$R_{in}$		100		$\Omega$	2
Single ended data input swing	$V_{in,pp}$	90		800	mV	
Transmit Disable Voltage	$V_D$	2		$V_{CC}$	V	3
Transmit Enable Voltage	$V_{EN}$	$V_{EE}$		$V_{EE} + 0.8$	V	
<b>Receiver</b>						
Single ended data output swing	$V_{out,pp}$	185		425	mV	4
LOS Fault	$V_{LOS\ fault}$	2		$V_{CC\ HOST}$	V	5
LOS Normal	$V_{LOS\ norm}$	$V_{EE}$		$V_{EE} + 0.8$	V	5
Power Supply Rejection	PSR	100			mVpp	6
Deterministic Jitter @ 8.5 Gb/s	RX DJ			0.420	UI	
Deterministic Jitter @ 14.025 Gb/s	RX DJ			0.220	UI	

**Notes:**

1. With established link
2. Connected directly to TX data input pins. AC coupling from pins into laser driver IC.
3. Or open circuit.
4. Into 100 ohms differential termination.
5. LOS is an open collector output. Should be pulled up with 4.7k – 10kohms on the host board. Normal operation is logic 0; loss of signal is logic 1. Maximum pull-up voltage is 5.5V.
6. Receiver sensitivity is compliant with power supply sinusoidal modulation of 20 Hz to 1.5 MHz up to specified value applied through the recommended power supply filtering network.

VI. **Optical Characteristics** ( $T_A$ ,  $V_{CC} = 3.15$  to  $3.46$  Volts)

Parameter	Symbol	Min	Typ	Max	Unit	Ref.
<b>Transmitter</b>						
Average Output Power: 50 or 62.5 MMF	$P_{OUT}$	-7.8			dBm	1
Optical Wavelength	$\lambda$	840		860	nm	
Spectral Width (RMS) @ 14.025Gb/s	$\sigma$			0.59	nm	
Optical Modulation Amplitude @ 4.25 Gb/s	OMA	247			$\mu$ W	
Optical Modulation Amplitude @ 8.5 Gb/s	OMA	302			$\mu$ W	
Optical Modulation Amplitude @ 14.025 Gb/s	OMA	331			$\mu$ W	
Optical Rise/Fall Time @ 4.25 Gb/s	$t_r / t_f$			90	ps	2
Transmitter Waveform and Dispersion Penalty @ 8.5 Gb/s	TWDP			4.3	dB	3
Vertical Eye Closure Penalty @ 14.025 Gb/s	CEVPQ			2.56	dB	4
Relative Intensity Noise	RIN			-128	dB/Hz	
<b>Receiver</b>						
Receiver OMA Sensitivity = 4.25 Gb/s	RxSENS			61	$\mu$ W	5
Receiver OMA Sensitivity = 8.5 Gb/s	RxSENS			76	$\mu$ W	5
Receiver OMA Sensitivity = 14.025 Gb/s	RxSENS			89	$\mu$ W	5
Average Receiver Power	$Rx_{MAX}$	0			dBm	
Optical Center Wavelength	$\lambda_C$	770		860	nm	
Optical Return Loss		12			dB	
LOS De-Assert	$LOS_D$			-13	dBm	
LOS Assert	$LOS_A$	-30			dBm	
LOS Hysteresis		0.5			dB	

**Notes:**

- Class 1 Laser Safety limit per FDA/CDRH, and EN (IEC) 60825 laser safety standards.
- Unfiltered, 20-80%. Complies with FC 1x and 2x eye mask when filtered.
- TWDP is calculated with a 1,0 equalizer and a 9.84 GHz Gaussian filter for the fiber simulation. Jitter values at  $\gamma_T$  and  $\gamma_R$  are controlled by TWDP and stress receiver sensitivity.
- VECPQ is calculated with a 16.6 GHz Gaussian filter for fiber simulation
- Unstressed Rx sensitivity

## VI. General Specifications

Parameter	Symbol	Min	Typ	Max	Units	Ref.
Data Rate	BR		4.25, 8.5 14.025		Gb/sec	1
Bit Error Rate	BER			$10^{-12}$		2
Fiber Length on 50/125 $\mu$ m MMF	L			150 50 35	m	3 4 5
Fiber Length on 50/125 $\mu$ m high-bandwidth (OM3) MMF	L			380 150 100	m	3 4 5

### Notes:

1. 4x and 8x Fibre Channel compatible, per FC-PI-4<sup>1</sup>.
2. PRBS 2<sup>7</sup>-1 for 4GFC and 8GFC. PRBS 2<sup>31</sup>-1 for 16GFC
3. At 4.25 Gb/s Fibre Channel data rate.
4. At 8.5 Gb/s Fibre Channel data rate.
5. At 14.025 Gb/s Fibre Channel data rate.

## VI. Environmental Specifications

Parameter	Symbol	Min	Typ	Max	Units	Ref.
Case Operating Temperature	T <sub>op</sub>				°C	
Commercial Temperature		0		70		
Extended Temperature		0		85		
Storage Temperature	T <sub>sto</sub>	-40		85	°C	

## VII. Regulatory Compliance

Finisar transceivers are Class 1 Laser Products and comply with US FDA regulations. These products are certified by TÜV and CSA to meet the Class 1 eye safety requirements of EN (IEC) 60825 and the electrical safety requirements of EN (IEC) 60950. Copies of certificates are available at Finisar Corporation upon request.

## VIII. Digital Diagnostic Functions

Finisar FTLF8529P3BNV SFP+ transceivers support the 2-wire serial communication protocol as defined in the SFP MSA<sup>f</sup>. It is very closely related to the E<sup>2</sup>PROM defined in the GBIC standard, with the same electrical specifications.

The standard SFP serial ID provides access to identification information that describes the transceiver's capabilities, standard interfaces, manufacturer, and other information.

Additionally, Finisar SFP transceivers provide an enhanced digital diagnostic monitoring interface, which allows real-time access to device operating parameters such as transceiver temperature, laser bias current, transmitted optical power, received optical power and transceiver supply voltage. It also defines a sophisticated system of alarm and warning flags, which alerts end-users when particular operating parameters are outside of a factory set normal range.

The SFP MSA defines a 256-byte memory map in E<sup>2</sup>PROM that is accessible over a 2-wire serial interface at the 8 bit address 1010000X (A0h). The digital diagnostic monitoring interface makes use of the 8 bit address 1010001X (A2h), so the originally defined serial ID memory map remains unchanged. The interface is identical to, and is thus fully backward compatible with both the GBIC Specification and the SFP Multi Source Agreement. The complete interface is described in Finisar Application Note AN-2030: "Digital Diagnostics Monitoring Interface for SFP Optical Transceivers".

The operating and diagnostics information is monitored and reported by a Digital Diagnostics Transceiver Controller (DDTC) inside the transceiver, which is accessed through a 2-wire serial interface. When the serial protocol is activated, the serial clock signal (SCL, Mod Def 1) is generated by the host. The positive edge clocks data into the SFP transceiver into those segments of the E<sup>2</sup>PROM that are not write-protected. The negative edge clocks data from the SFP transceiver. The serial data signal (SDA, Mod Def 2) is bi-directional for serial data transfer. The host uses SDA in conjunction with SCL to mark the start and end of serial protocol activation. The memories are organized as a series of 8-bit data words that can be addressed individually or sequentially.

For more information, please see the SFP MSA documentation<sup>c,f</sup> and Finisar Application Note AN-2030.

Please note that evaluation board FDB-1027 is available with Finisar ModDEMO software that allows simple to use communication over the 2-wire serial interface.

## IX. Digital Diagnostic Specifications

FTLF8529P3BNV transceivers can be used in host systems that require either internally or externally calibrated digital diagnostics.

Parameter	Symbol	Min	Typ	Max	Units	Ref.
<b>Accuracy</b>						
Internally measured transceiver temperature	DD <sub>Temperature</sub>			3	°C	
Internally measured transceiver supply voltage	DD <sub>Voltage</sub>			100	mV	
Measured TX bias current	DD <sub>Bias</sub>			10	%	1
Measured TX output power	DD <sub>Tx-Power</sub>			2	dB	
Measured RX received average optical power	DD <sub>Rx-Power</sub>			2	dB	
<b>Dynamic Range for Rated Accuracy</b>						
Internally measured transceiver temperature	DD <sub>Temperature</sub>	-0		85	°C	
Internally measured transceiver supply voltage	DD <sub>Voltage</sub>	3.15		3.46	V	
Measured TX bias current	DD <sub>Bias</sub>	0		20	mA	
Measured TX output power	DD <sub>Tx-Power</sub>	-8		-1	dBm	
Measured RX received average optical power	DD <sub>Rx-Power</sub>	-14		0	dBm	
<b>Max Reporting Range</b>						
Internally measured transceiver temperature	DD <sub>Temperature</sub>	-40		125	°C	
Internally measured transceiver supply voltage	DD <sub>Voltage</sub>	2.8		4.0	V	
Measured TX bias current	DD <sub>Bias</sub>	0		20	mA	
Measured TX output power	DD <sub>Tx-Power</sub>	-10		-2	dBm	
Measured RX received average optical power	DD <sub>Rx-Power</sub>	-20		0	dBm	

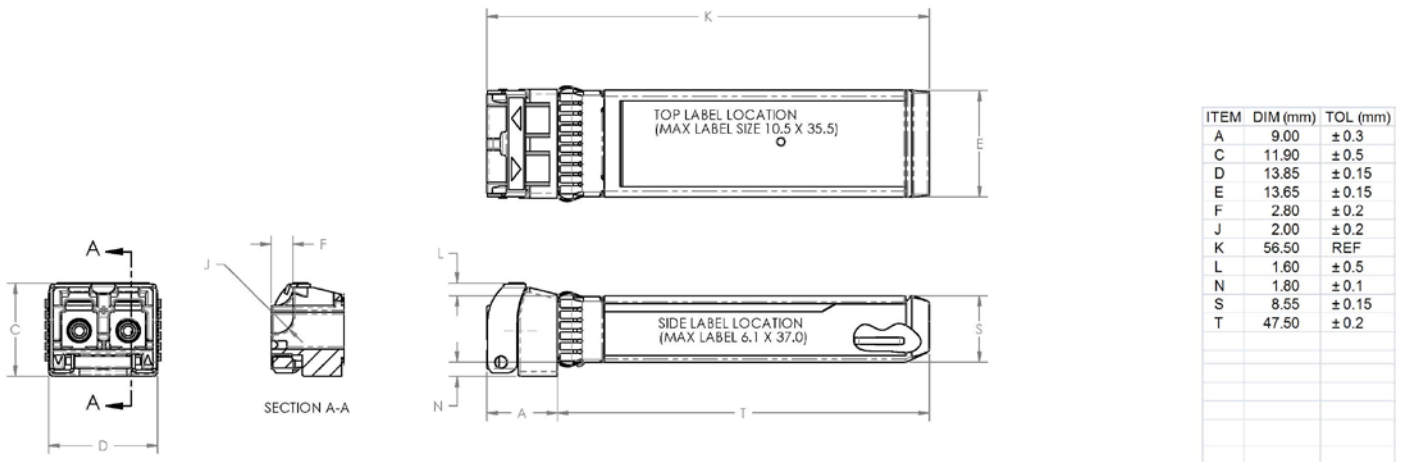
### Notes:

- Accuracy of Measured Tx Bias Current is 10% of the actual Bias Current from the laser driver to the laser.



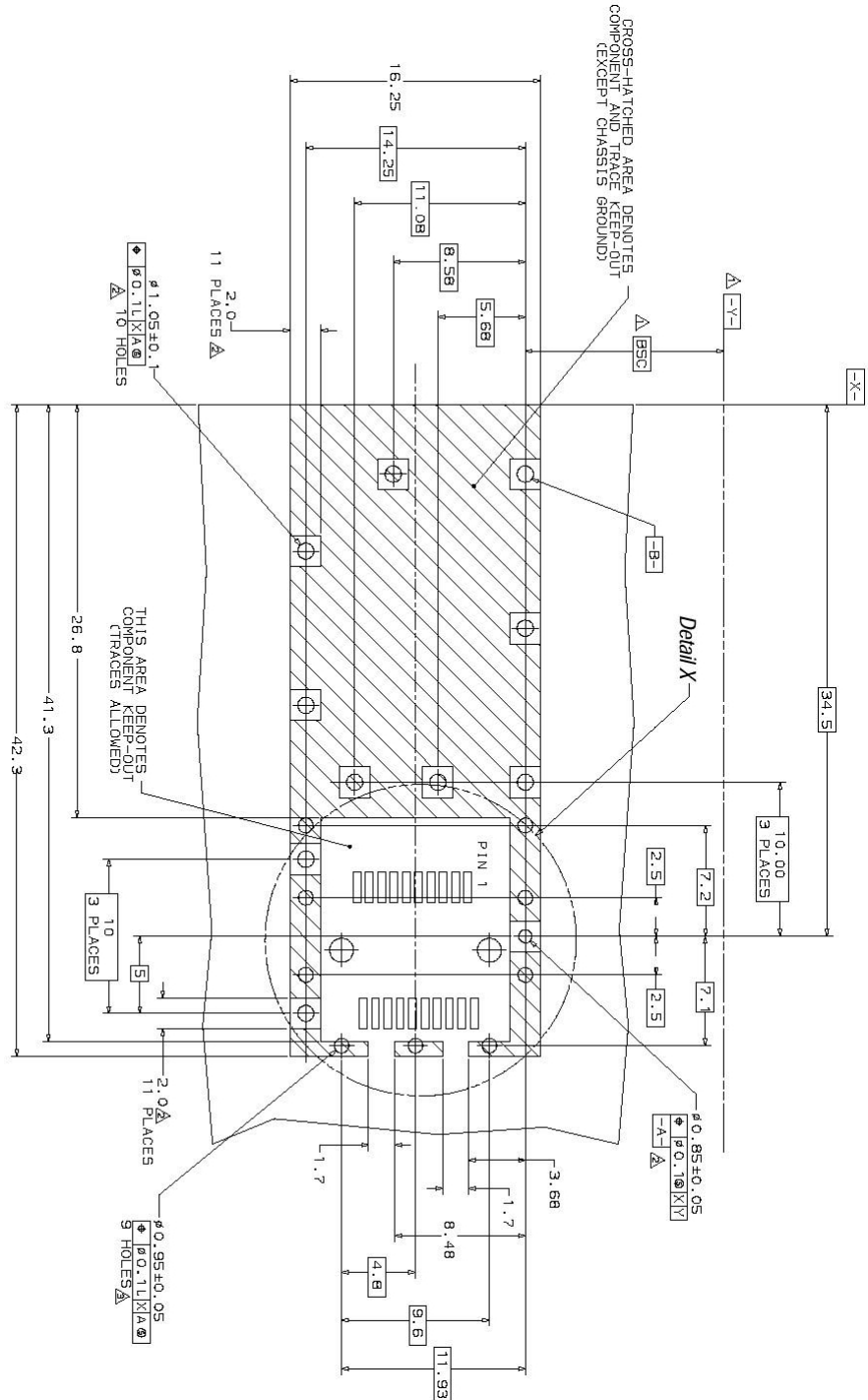
**X. Mechanical Specifications**

Finisar’s FTLF8529P3BNV SFP+ transceivers are compatible with the SFF-8432<sup>b</sup> specification for improved pluggable form factor.

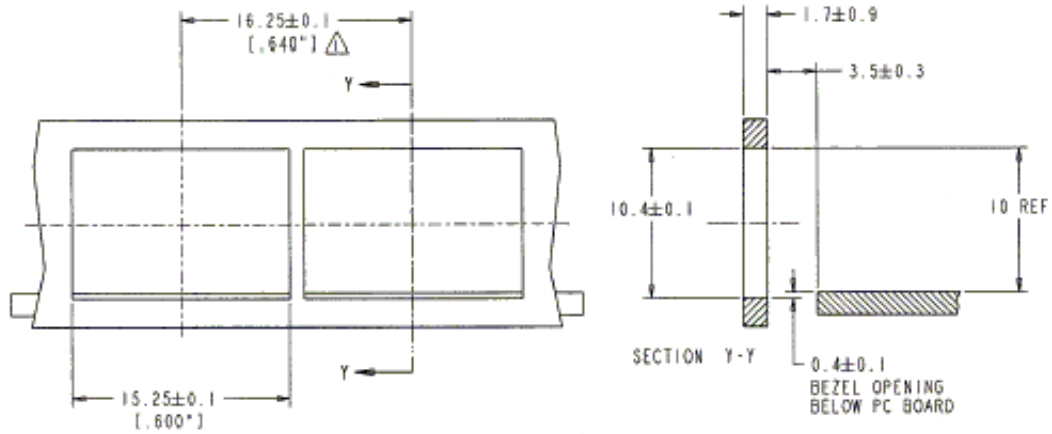
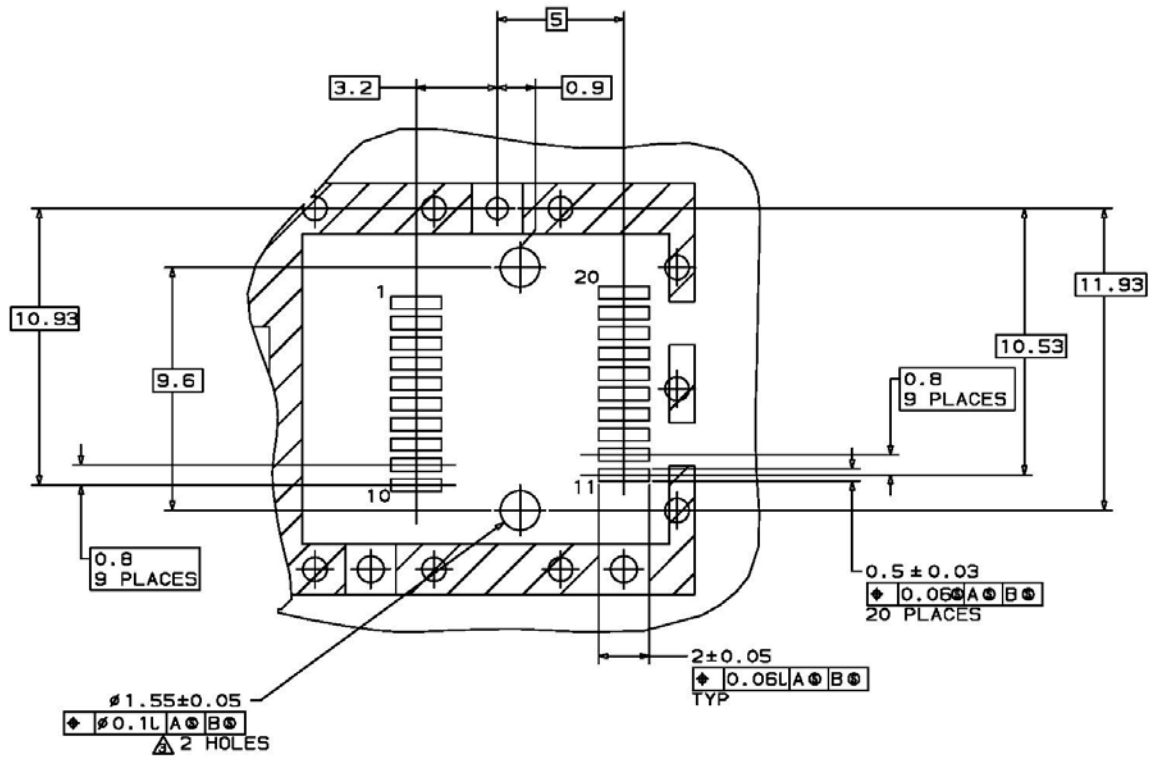


**FTLF8529P3BNV**

### XI. PCB Layout and Bezel Recommendations



- Datum and Basic Dimension Established by Customer
- Pads and Vias are Chassis Ground, 11 Places
- Through Holes are Unplated



NOTES:

1. MINIMUM PITCH ILLUSTRATED, ENGLISH DIMENSIONS ARE FOR REFERENCE ONLY

2. NOT RECOMMENDED FOR PCI EXPANSION CARD APPLICATIONS

**XII. References**

- a. “Fibre Channel Physical Interface-4 Specification (FC-PI-5 Rev. 6.00)”. American National Standard for Information Systems, To Be Published.
- b. “Improved Pluggable Form Factor”, SFF Document Number SFF-8432, Revision 5.0, July 16, 2007.
- c. “Digital Monitoring Interface for Optical Transceivers”, SFF Document Number SFF-8472, Revision 11.0.
- d. “Enhanced Small Form Factor Pluggable Module ‘SFP+ ‘”, SFF Document Number SFF-8431, Revision 4.1, July 6, 2009.
- e. Directive 2011/65/EU of the European Council Parliament and of the Council, “on the restriction of the use of certain hazardous substances in electrical and electronic equipment”. Certain products may use one or more exemptions as allowed by the Directive.
- f. Small Form Factor Pluggable (SFP) Transceiver Multi-source Agreement (MSA), September 14, 2000 which is available as SFF Document Number INF-8074, Revision 1.0 May 12, 2001
- g. “0.8mm Card Edge Connector for 16Gb/s Applications”, SFF Document Number SFF-8081, Revision 0.2, December 21, 2009.
- h. SFF-8081, Revision 0.2, Revision 0.9

**XIII. For More Information**

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